Politechnika Rzeszowska, Wydział Elektrotechniki i Informatyki, ul. Wincentego Pola 2, 35-959 Rzeszów, semestr zimowy r. ak. 2020/21

	FS2-L2										
	Poniedziałek A	**Poniedziałek B**	**Wtorek A**	**Wtorek B**	**Środa A**	**Środa B**	**Czwartek A**	**Czwartek B**	**Piątek A**	**Piątek B**	
@07:00-7:15	---	---	---	---	---	---	---	---	---	---	**@07:00-7:15**
07:15-07:30	---	---	---	---	---	---	---	---	---	---	**07:15-07:30**
07:30-07:45	---	---	---	---	---	---	---	---	---	---	**07:30-07:45**
07:45-08:00	---	---	---	---	---	---	---	---	---	---	**07:45-08:00**
08:00:08:15	---	---	---	---	---	---	---	---	---	---	**08:00:08:15**
08:15-08:30	---	---	---	---	---	---	---	---	---	---	**08:15-08:30**
08:30-08:45	---	---	---	---	---	---	---	---	---	---	**08:30-08:45**
@08:45-09:00	---	---	---	---	---	---	---	---	---	---	**@08:45-09:00**
09:00-09:15	---	---	---	---	---	---	---	---	---	---	**09:00-09:15**
09:15-09:30	---	---	---	---	---	---	---	---	---	---	**09:15-09:30**
09:30-09:45	---	---	---	---	---	---	---	---	---	---	**09:30-09:45**
09:45-10:00	---	---	---	---	---	---	---	---	---	---	**09:45-10:00**
10:00-10:15	---	---	---	---	---	---	---	---	---	---	**10:00-10:15**
10:15-10:30	---	---	---	---	---	---	---	---	---	---	**10:15-10:30**
@10:30-10:45	FS2 Projektowanie systemów i sieci komputerowych (FS2-DI) - W20 Wykład Bolanowski Marek (ES), Paszkiewicz Andrzej (ES)	FS2 Projektowanie systemów i sieci komputerowych (FS2-DI) - W20 Wykład Bolanowski Marek (ES), Paszkiewicz Andrzej (ES)	---	---	---	---	---	---	---	---	**@10:30-10:45**
10:45-11:00			---	---	---	---	---	---	---	---	**10:45-11:00**
11:00-11:15			---	---	---	---	---	---	---	---	**11:00-11:15**
11:15-11:30			---	---	---	---	---	---	---	---	**11:15-11:30**
11:30-11:45			---	---	---	---	---	---	---	---	**11:30-11:45**
11:45-12:00			---	---	---	---	---	---	---	---	**11:45-12:00**
12:00-12:15			---	---	---	---	---	---	---	---	**12:00-12:15**
@12:15-12:30	---	---	---	---	---	---	---	---	---	---	**@12:15-12:30**
12:30-12:45	---	---	---	---	---	---	---	---	---	---	**12:30-12:45**
12:45-13:00	---	---	---	---	---	---	---	---	---	---	**12:45-13:00**
13:00-13:15	---	---	---	---	---	---	---	---	---	---	**13:00-13:15**
13:15-13:30	---	---	---	---	---	---	---	---	---	---	**13:15-13:30**
13:30-13:45	---	---	---	---	---	---	---	---	---	---	**13:30-13:45**
13:45-14:00	---	---	---	---	---	---	---	---	---	---	**13:45-14:00**
@14:00-14:15	---	---	FS2 Bazy danych (FS2-DI) - W30 Wykład Dymora Paweł (ES)	FS2 Bazy danych (FS2-DI) - W30 Wykład Dymora Paweł (ES)	---	---	---	---	---	---	**@14:00-14:15**
14:15-14:30	---	---			---	---	---	---	---	---	**14:15-14:30**
14:30-14:45	---	---			---	---	---	---	---	---	**14:30-14:45**
14:45-15:00	---	---			---	---	---	---	---	---	**14:45-15:00**
15:00-15:15	---	---			---	---	---	---	---	---	**15:00-15:15**
15:15-15:30	---	---			---	---	---	---	---	---	**15:15-15:30**
15:30-15:45	---	---			---	---	---	---	---	---	**15:30-15:45**
@15:45-16:00	---	---	---	Bazy danych (FS2-DI) - P10 Projekt Dymora Paweł (ES)	---	---	---	---	---	---	**@15:45-16:00**
16:00-16:15	---	---	---		---	---	---	---	---	---	**16:00-16:15**
16:15-16:30	---	---	---		---	---	---	---	---	---	**16:15-16:30**
16:30-16:45	---	---	---		---	---	---	---	---	---	**16:30-16:45**
16:45-17:00	---	---	---		---	---	---	---	---	---	**16:45-17:00**
17:00-17:15	---	---	---		---	---	---	---	---	---	**17:00-17:15**
17:15-17:30	---	---	---		---	---	---	---	---	---	**17:15-17:30**
@17:30-17:45	Bazy danych (FS2-DI) - L30 Laboratorium Hadaj Piotr (ES)	Bazy danych (FS2-DI) - L30 Laboratorium Hadaj Piotr (ES)	Projektowanie systemów i sieci komputerowych (FS2-DI) - L15 Laboratorium Wroński Michał (ES)	Projektowanie systemów i sieci komputerowych (FS2-DI) - P15 Projekt Paszkiewicz Andrzej (ES)	---	---	---	---	---	---	**@17:30-17:45**
17:45-18:00					---	---	---	---	---	---	**17:45-18:00**
18:00-18:15					---	---	---	---	---	---	**18:00-18:15**
18:15-18:30					---	---	---	---	---	---	**18:15-18:30**
18:30-18:45					---	---	---	---	---	---	**18:30-18:45**
18:45-19:00					---	---	---	---	---	---	**18:45-19:00**
19:00-19:15					---	---	---	---	---	---	**19:00-19:15**
	Timetable generated with FET 5.45.1 on 11/25/20 7:43 PM										

Projekt i realizacja: Albert Żmuda
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